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Serial Number: 10 / 750, 871

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 **PALM INTRANET**

Inventor Information for 10/750871

| Inventor Name | City | State/Country |
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[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity Data](#)[Foreign Data](#)[Inventors](#)

Search Another: Application# [Search](#) or Patent# [Search](#)
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Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

| | | | | | |
|-------------------------|----------|--|------------|---------------------------------|---------------------------|
| US 20060120487 A1 | 20060608 | Frequency offset estimating method and frequency offset correcting apparatus utilizing said method | 375/334 | | Nakao; Seigo et al. |
| US 20060080027 A1 | 20060413 | Engine starting assist system | 701/113 | | Tanaka; Yasuhiro et al. |
| US 20060080026 A1 | 20060413 | Auxiliary system for starting engine | 701/113 | | Tanaka; Yasuhiro et al. |
| US 20060077791 A1 | 20060413 | Objective lens element and optical pickup device provided with its object lens element | 369/44.23 | 369/112.23 | Takano; Hironari et al. |
| US 20060072638 A1 | 20060406 | External cavity type tunable laser source | 372/38.02 | 372/43.01 | Tanaka; Yasuhiro |
| US 20060071381 A1 | 20060406 | Liquid-sealed vibration control equipment and elastic partition film for use therein | 267/140.13 | | Hatakeyama; Shingo et al. |
| US 20060060288 A1 | 20060323 | Adhesive and method of manufacturing image display apparatus using the same | 156/163 | | Maeda; Reiko et al. |
| US 20060055753 A1 | 20060316 | Recording liquid, liquid cartridge, liquid discharge apparatus and method of liquid discharge | 347/100 | | Sekiguchi; Hideki et al. |
| US 20060044079 A1 | 20060302 | Surface acoustic wave device | 333/193 | | Nishizawa; Toshio et al. |
| US 20060022767 A1 | 20060202 | Surface acoustic wave device | 333/133 | | Taniguchi; Noriaki et al. |
| US 20050286562 A1 | 20051229 | Method and apparatus for transmitting signals, and method and apparatus for receiving the signals | 370/477 | | Nakao, Seigo et al. |
| US 20050286484 A1 | 20051229 | Method and apparatus for transmitting signals | 370/349 | | Nakao, Seigo et al. |
| US 20050280907 A1 | 20051222 | Optical pickup lens device and information recording and reproducing device using the same | 359/754 | | Hayashi, Katano et al. |
| US 20050250796 A1 | 20051110 | Heterocyclic compounds | 514/264.1 | 514/300; 544/279; 546/122 | Yamada, Tatsuhiro et al. |

| | | | | | |
|-------------------------|----------|---|-----------|--|--------------------------|
| US 20050225619 A1 | 20051013 | Ink used in inkjet recording, method for inkjet recording, inkjet recording head and manufacturing method therefor, method for treating inkjet recording head, and inkjet printer | 347/100 | | Fukuda, Toshio et al. |
| US 20050222141 A1 | 20051006 | Novel phenylalanine derivatives | 514/227.8 | 514/234.2; 514/266.22; 514/266.31; 544/114; 544/284; 544/285; 544/60 | Sagi, Kazuyuki et al. |
| US 20050202958 A1 | 20050915 | Catalysts for polymerization or copolymerization of alpha-olefins, catalyst components thereof, and processes for polymerization of alpha-olefins with the catalysts | 502/118 | 502/103; 502/124; 502/167; 526/125.3; 526/128; 526/905 | Yoshikiyo, Motozo et al. |
| US 20050164655 A1 | 20050728 | Method and apparatus for transmitting signals, method and apparatus for receiving the signals, and communication system utilizing the same | 455/101 | | Nakao, Seigo et al. |
| US 20050163264 A1 | 20050728 | Correlator and receiving apparatus utilizing the same | 375/343 | | Nakao, Seigo et al. |
| US 20050161584 A1 | 20050728 | Solid-state imaging device and camera | 250/208.1 | 250/214.1; 257/E31.128 | Tanaka, Syouji et al. |
| US 20050153527 A1 | 20050714 | Method of manufacturing semiconductor device | 438/514 | 257/E29.117; 257/E29.147 | Kitamura, Akio |
| US 20050141658 A1 | 20050630 | Frequency offset estimating method and frequency offset correcting apparatus utilizing said method | 375/346 | | Tanaka, Yasuhiro et al. |
| US 20050141641 A1 | 20050630 | Receiving method and receiving apparatus with adaptive array signal processing | 375/316 | | Tanaka, Yasuhiro et al. |
| US 20050127983 A1 | 20050616 | Voltage booster device having voltage-suppressing circuit | 327/536 | | Takagi, Nobutomo et al. |
| US 20050127439 | 20050616 | Semiconductor device exhibiting a high breakdown voltage and | 257/341 | 257/E21.346; 257/E21.417; | Matsuzaki, Kazuo et al. |

| | | | | | |
|-------------------------|----------|---|------------|---|-------------------------------|
| A1 | | the method of manufacturing the same | | 257/E29.012; 257/E29.04; 257/E29.062; 257/E29.256 | |
| US 20050122597 A1 | 20050609 | Optical head device and optical information recording and reproducing device | 359/754 | | Tanaka, Yasuhiro et al. |
| US 20050111336 A1 | 20050526 | Objective lens element for optical disks and optical head device incorporating the same | 369/112.08 | 369/112.23 | Tanaka, Yasuhiro et al. |
| US 20050093210 A1 | 20050505 | Method for producing optical element having antireflection structure, and optical element having antireflection structure produced by the method | 264/528 | 264/454; 264/500 | Umetani, Makoto et al. |
| US 20050088746 A1 | 20050428 | Light quantity distribution control element and optical apparatus using the same | 359/569 | 359/566 | Yoshikawa, Motonobu et al. |
| US 20050086790 A1 | 20050428 | Press-in apparatus for Z profile sheet pile and press-in method of Z profile sheet pile | 29/509 | 29/715 | Kitamura, Akio et al. |
| US 20050070485 A1 | 20050331 | Phenylalanine derivatives | 514/19 | 562/450 | Tanaka, Yasuhiro et al. |
| US 20050046555 A1 | 20050303 | Condition monitor system responsive to different input pulses | 340/438 | 307/10.1; 340/659 | Takagi, Nobutomo et al. |
| US 20050033494 A1 | 20050210 | Electronic control unit having hold circuit and method therefor | 701/36 | 307/10.7 | Harada, Takashi et al. |
| US 20050020040 A1 | 20050127 | Manufacturing method of a semiconductor device | 438/534 | 257/E21.345; 257/E21.427; 257/E29.021; 257/E29.268 | Yamaji, Masaharu et al. |
| US 20050018586 A1 | 20050127 | Objective lens for optical disk, optical head device and optical information recording and reproducing apparatus using the same, and mold for molding lens, method for working the mold for molding lens, and profile measuring device for measuring profile of the mold for molding lens | 369/112.23 | | Tanaka, Yasuhiro et al. |
| US 20040232498 | 20041125 | Semiconductor integrated circuit device and manufacture method | 257/391 | 257/E21.544; 257/E21.618; | Kitamura, Akio |

| | | | | | |
|-------------------------|----------|---|------------|---|----------------------------|
| A1 | | therefore | | 257/E21.624; 257/E21.63; 257/E21.696; 257/E27.017 | |
| US 20040147546 A1 | 20040729 | Heterocyclic compounds | 514/303 | | Tanaka, Yasuhiro et al. |
| US 20040145997 A1 | 20040729 | Optical head, optical information recording/reproducing apparatus, computer, video recording/reproducing apparatus, video reproducing apparatus, server and car navigation system | 369/112.24 | | Yasuda, Akihiro et al. |
| US 20040145640 A1 | 20040729 | Ink used in inkjet recording, method for inkjet recording, inkjet recording head and manufacturing method therefor, method for treating inkjet recording head, and inkjet printer | 347/100 | | Fukuda, Toshio et al. |
| US 20040109242 A1 | 20040610 | Optical element, optical lens, optical head apparatus, optical information apparatus, computer, optical information medium player, car navigation system, optical information medium recorder, and optical information medium server | 359/738 | | Komma, Yoshiaki et al. |
| US 20040108551 A1 | 20040610 | Semiconductor device | 257/342 | 257/E29.009; 257/E29.021; 257/E29.063; 257/E29.268 | Kitamura, Akio et al. |
| US 20040051137 A1 | 20040318 | Semiconductor integrated circuit device and method of manufacturing the same | 257/344 | 257/E21.638 | Kitamura, Akio |
| US 20040042186 A1 | 20040304 | Surface-mounted electronic component module and method for manufacturing the same | 361/760 | 257/E21.705; 257/E25.012; 257/E25.031 | Furukawa, Osamu et al. |
| US 20040013430 A1 | 20040122 | Wavelength monitor and motor driving and controlling device | 398/65 | | Asami, Keisuke et al. |
| US 20040004913 A1 | 20040108 | Optical element, optical head, method for correcting spherical aberration, and optical | 369/44.32 | 369/112.23 | Wada, Hidenori et al. |

| | | | | | |
|-------------------------|----------|--|---------|---|-------------------------------|
| | | recording/reproducing apparatus | | | |
| US 20030229440 A1 | 20031211 | Control apparatus for vehicle | 701/114 | 477/110; 701/102; 701/110 | Tanaka, Yasuhiro et al. |
| US 20030220726 A1 | 20031127 | Vehicle electronic controller | 701/36 | 701/1 | Yokochi, Masaru et al. |
| US 20030214769 A1 | 20031120 | Over-voltage protection circuit | 361/56 | | Nishikawa, Mutsuo et al. |
| US 20030208389 A1 | 20031106 | Production planning method and system for preparing production plan | 705/7 | | Kurihara, Hideshi et al. |
| US 20030207509 A1 | 20031106 | Semiconductor integrated circuit device and manufacture method thereof | 438/199 | 257/E21.634; 257/E27.063 | Kitamura, Akio |
| US 20030164679 A1 | 20030904 | Organic electroluminescence element and image forming apparatus or portable terminal unit using thereof | 313/504 | | Hamano, Takafumi et al. |
| US 20030149083 A1 | 20030807 | Phenylalanine derivatives | 514/357 | 514/428; 514/517; 514/538; 514/562; 514/564; 514/567; 546/335; 548/561; 558/48; 560/12; 560/34; 560/41; 562/430 | Tanaka, Yasuhiro et al. |
| US 20030122163 A1 | 20030703 | Semiconductor device | 257/213 | 257/E29.021; 257/E29.04; 257/E29.268 | Kitamura, Akio |
| US 20030114490 A1 | 20030619 | New phenylalanine derivatives | 514/317 | 514/561; 514/562; 546/226; 562/426; 562/439 | Tanaka, Yasuhiro et al. |
| US 20030035226 A1 | 20030220 | Objective lens and optical head device | 359/719 | | Sasano, Tomohiko et al. |
| US 20030012496 | 20030116 | COUPLING LENS AND SEMICONDUCTOR LASER | 385/33 | 385/37; 385/93 | YAMAGATA, MICHIIHIRO |

| | | | | | |
|-------------------------|----------|--|----------|--|-------------------------------|
| A1 | | MODULE | | | et al. |
| US 20030001202 A1 | 20030102 | Semiconductor device | 257/330 | 257/E29.009; 257/E29.021; 257/E29.13; 257/E29.133; 257/E29.256; 257/E29.268 | Kitamura, Akio |
| US 20020197782 A1 | 20021226 | Method of manufacturing semiconductor device | 438/200 | 257/E29.117; 257/E29.147 | Kitamura, Akio |
| US 20020196428 A1 | 20021226 | Device for calculating diffraction efficiencies of a diffraction lens, lens with grating element, and optical system for reading | 356/124 | 359/569; 382/286 | Yamagata, Michihiro et al. |
| US 20020190341 A1 | 20021219 | Semiconductor device with a protective diode having a high breakdown voltage | 257/499 | 257/E29.064; 257/E29.133; 257/E29.268 | Kitamura, Akio |
| US 20020186518 A1 | 20021212 | Overvoltage protection circuit | 361/91.1 | 257/E29.268 | Nishikawa, Mutsuo et al. |
| US 20020186476 A1 | 20021212 | Optical system for optical disk, optical head unit for optical disk, and optical drive unit | 359/719 | 359/717; 359/718 | Sasano, Tomohiko et al. |
| US 20020166952 A1 | 20021114 | Beam shaper, and semiconductor laser source device and optical head using the beam shaper | 250/216 | | Tanaka, Yasuhiro et al. |
| US 20020145172 A1 | 20021010 | High withstand voltage semiconductor device | 257/487 | 257/E29.009; 257/E29.012; 257/E29.027; 257/E29.04; 257/E29.116; 257/E29.133; 257/E29.256; 257/E29.268 | Fujishima, Naoto et al. |
| US 20020135028 A1 | 20020926 | Semiconductor integrated circuit device and method of manufacturing the same | 257/390 | 257/E21.638 | Kitamura, Akio |
| US 20020117723 A1 | 20020829 | Semiconductor integrated circuit device and manufacture method therefore | 257/391 | 257/E21.544; 257/E21.618; 257/E21.624; 257/E21.63; 257/E21.696; 257/E27.017 | Kitamura, Akio |
| US | 20020829 | Semiconductor integrated circuit | 257/336 | 257/1; | Kitamura, |

| | | | | | |
|-------------------------|----------|--|---------|--|-------------------------------|
| 20020117713 A1 | | device and manufacture method therefore | | 257/E21.634; 257/E27.063 | Akio |
| US 20020106154 A1 | 20020808 | Apparatus for and method of jointing probes | 385/33 | 372/109; 372/9; 385/31 | Tanaka, Yasuhiro |
| US 20020079981 A1 | 20020627 | Center-electrode assembly and manufacturing method therefor, nonreciprocal circuit device and communication apparatus using the same | 333/1.1 | 333/24.2 | Tanaka, Yasuhiro |
| US 20020067650 A1 | 20020606 | Semiconductor memory with built-in cache | 365/222 | 365/189.05 | Tanaka, Yasuhiro et al. |
| US 20020060920 A1 | 20020523 | Semiconductor memory with built-in cache | 365/49 | | Tanaka, Yasuhiro et al. |
| US 20020003288 A1 | 20020110 | Semiconductor apparatus and method for manufacturing the same | 257/640 | 257/649; 257/E23.134; 257/E29.015; 438/724 | Saitou, Masaru et al. |
| US 20010053103 A1 | 20011220 | Semiconductor memory with built-in cache | 365/207 | | Tanaka, Yasuhiro et al. |
| US 20010050873 A1 | 20011213 | Semiconductor memory with built-in cache | 365/205 | 365/207; 365/222; 365/49 | Tanaka, Yasuhiro et al. |
| US 20010048122 A1 | 20011206 | Semiconductor device | 257/240 | 257/296; 257/367 | Tada, Gen et al. |
| US 20010040815 A1 | 20011115 | Ferroelectric memory device and method for operating ferroelectric memory device | 365/145 | | Tanaka, Yasuhiro |
| US 20010038122 A1 | 20011108 | Semiconductor device exhibiting a high breakdown voltage and the method of manufacturing the same | 257/339 | 257/E21.346; 257/E21.417; 257/E29.012; 257/E29.04; 257/E29.062; 257/E29.256; 438/212; 438/268 | Matsuzaki, Kazuo et al. |
| US 20010036014 A1 | 20011101 | Lens array | 359/619 | 359/621; 359/622 | Sasano, Tomohiko et al. |
| US 20010026110 A1 | 20011004 | Rotor and sealing device for rotary adsorber | 310/261 | | Kurosawa, Masaji et al. |

| | | | | | |
|-------------------------|----------|---|------------|---|-------------------------------|
| US 20010019046 A1 | 20010906 | Method and apparatus for engraving | 219/121.85 | 219/121.6 | Tanaka, Yasuhiro et al. |
| US 20010008845 A1 | 20010719 | Gaming machine and operation method therefor | 463/25 | | Kusuda, Kazuhiro et al. |
| US 7068445 B2 | 20060627 | Optical pickup lens device and information recording and reproducing device using the same | 359/719 | 359/754; 369/44.32 | Hayashi; Katsuhiko et al. |
| US 7049202 B2 | 20060523 | Method of manufacturing semiconductor device | 438/390 | 257/390; 257/391; 257/392; 438/372; 438/373; 438/391 | Kitamura; Akio |
| US 7046350 B2 | 20060516 | Device for calculating diffraction efficiencies of a diffraction lens, lens with grating element, and optical system for reading | 356/124 | 359/569 | Yamagata; Michihiro et al. |
| US 7020550 B2 | 20060328 | Vehicle electronic controller | 701/36 | 123/396; 701/29; 708/530; 714/21 | Yokochi; Masaru et al. |
| US 6998680 B2 | 20060214 | Semiconductor device | 257/342 | 257/300; 257/340; 257/E29.009; 257/E29.021; 257/E29.063; 257/E29.268 | Kitamura; Akio et al. |
| US 6921162 B2 | 20050726 | Ink used in inkjet recording, method for inkjet recording, inkjet recording head and manufacturing method therefor, method for treating inkjet recording head, and inkjet printer | 347/95 | 347/45; 347/96 | Fukuda; Toshio et al. |
| US 6914496 B2 | 20050705 | Center-electrode assembly and manufacturing method therefor, nonreciprocal circuit device and communication apparatus using the same | 333/24.2 | 333/1.1 | Tanaka; Yasuhiro |
| US 6905410 B2 | 20050614 | Gaming machine and operation method therefor | 463/25 | | Kusuda; Kazuhiro et al. |
| US 6900081 | 20050531 | Method of manufacturing | 438/129 | 257/E21.638; | Kitamura; |

| | | | | | |
|------------------|----------|---|------------|--|----------------------------|
| B2 | | semiconductor integrated circuit device | | 438/228; 438/275 | Akio |
| US 6861702 B2 | 20050301 | Semiconductor device | 257/330 | 257/335; 257/336; 257/340; 257/343; 257/E29.009; 257/E29.021; 257/E29.13; 257/E29.133; 257/E29.256; 257/E29.268 | Kitamura; Akio |
| US 6855706 B2 | 20050215 | Phenylalanine derivatives | 514/210.17 | 514/217.11; 514/224.2; 514/230.5; 514/231.2; 514/249; 514/255.01; 514/327; 514/330; 514/423; 514/563; 540/607; 544/105; 544/168; 544/169; 544/349; 544/391; 544/41; 546/221; 546/225; 548/539; 548/540; 548/953; 562/433; 562/445; 562/447; 562/448 | Tanaka; Yasuhiro et al. |
| US 6853034 B2 | 20050208 | Semiconductor device exhibiting a high breakdown voltage and the method of manufacturing the same | 257/343 | 257/492; 257/493; 257/E21.346; 257/E21.417; 257/E29.012; 257/E29.04; 257/E29.062; 257/E29.256 | Matsuzaki; Kazuo et al. |

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|------------------|----------|---|------------|--|----------------------------|
| US 6833667 B2 | 20041221 | Organic electroluminescence element and image forming apparatus or portable terminal unit using thereof | 313/504 | 313/113; 313/506 | Hamano; Takafumi et al. |
| US 6833594 B2 | 20041221 | Semiconductor integrated circuit device and manufacture method therefore | 257/391 | 257/497; 257/498; 257/E21.544; 257/E21.618; 257/E21.624; 257/E21.63; 257/E21.696; 257/E27.017 | Kitamura; Akio |
| US 6829209 B1 | 20041207 | OBJECTIVE FOR OPTICAL DISK, OPTICAL HEAD DEVICE AND OPTICAL INFORMATION RECORDING/REPRODUCING APPARATUS BOTH COMPRISING THE SAME, MOLD FOR FORMING LENS, METHOD FOR MACHINING MOLD FOR FORMING LENS, AND SHAPE MEASURING INSTRUMENT | 369/112.23 | | Tanaka; Yasuhiro et al. |
| US 6809376 B2 | 20041026 | Semiconductor integrated circuit device and manufacture method therefore | 257/336 | 257/335; 257/E21.634; 257/E27.063; 438/301 | Kitamura; Akio |
| US 6807017 B2 | 20041019 | Objective lens and optical head device | 359/719 | 359/718; 369/44.23 | Sasano; Tomohiko et al. |
| US 6775609 B2 | 20040810 | Electronic control unit for vehicle having operation monitoring function and fail-safe function | 701/114 | 123/396; 701/115 | Ozeki; Yoshifumi et al. |
| US 6740952 B2 | 20040525 | High withstand voltage semiconductor device | 257/487 | 257/496; 257/E29.009; 257/E29.012; 257/E29.027; 257/E29.04; 257/E29.116; 257/E29.133; 257/E29.256; 257/E29.268 | Fujishima; Naoto et al. |
| US 6730961 B2 | 20040504 | Semiconductor device | 257/330 | 257/329; 257/331; | Kitamura; Akio |

| | | | | | |
|------------------|----------|---|---------|---|-------------------------------|
| | | | | 257/E29.021; 257/E29.04; 257/E29.268; 438/259; 438/270 | |
| US 6710671 B1 | 20040323 | Nonreciprocal circuit device and method of fabricating the same | 333/1.1 | 333/24.2 | Tanaka; Yasuhiro |
| US 6639287 B2 | 20031028 | Semiconductor integrated circuit device | 257/390 | 257/369; 257/E21.638 | Kitamura; Akio |
| US 6634159 B1 | 20031021 | Taping apparatus for electronic components | 53/591 | 53/246 | Muto; Kenichi et al. |
| US 6627869 B2 | 20030930 | Beam shaper, and semiconductor laser source device and optical head using the beam shaper | 250/216 | 359/711 | Tanaka; Yasuhiro et al. |
| US 6610710 B2 | 20030826 | Phenylalanine derivatives | 514/330 | 514/331; 514/538; 514/561; 514/563; 514/567; 514/575; 544/316; 546/226; 546/245; 548/127; 560/37; 560/38; 560/39; 560/40; 560/45; 560/47; 562/444; 562/445; 562/446; 562/447; 562/448; 562/449; 562/621 | Tanaka; Yasuhiro et al. |
| US 6606198 B2 | 20030812 | Lens array | 359/619 | | Sasano; Tomohiko et al. |
| US 6590717 B2 | 20030708 | Optical system for optical disk, optical head unit for optical disk, and optical drive device | 359/717 | 369/112.23; 398/178 | Sasano; Tomohiko et al. |
| US 6587366 B2 | 20030701 | Ferroelectric memory device and method for operating | 365/145 | 365/149; 365/230.06 | Tanaka; Yasuhiro |

| | | | | | |
|---------------|----------|--|-----------|--|-------------------------------|
| | | ferroelectric memory device | | | |
| US 6570237 B2 | 20030527 | Semiconductor device with a protective diode having a high breakdown voltage | 257/499 | 257/500; 257/501; 257/502; 257/509; 257/577; 257/E29.064; 257/E29.133; 257/E29.268 | Kitamura; Akio |
| US 6558983 B2 | 20030506 | Semiconductor apparatus and method for manufacturing the same | 438/133 | 257/E23.134; 257/E29.015; 438/421 | Saitou; Masaru et al. |
| US 6527837 B2 | 20030304 | Rotor and sealing device for rotary adsorber | 96/125 | 55/502 | Kurosawa; Masaji et al. |
| US 6525390 B2 | 20030225 | MIS semiconductor device with low on resistance and high breakdown voltage | 257/489 | 257/490 | Tada; Gen et al. |
| US 6522563 B2 | 20030218 | Semiconductor memory with built-in cache | 365/49 | 365/222; 365/230.09 | Tanaka; Yasuhiro et al. |
| US 6510070 B2 | 20030121 | Semiconductor memory with built-in cache | 365/49 | 365/230.09 | Tanaka; Yasuhiro et al. |
| US 6504975 B1 | 20030107 | Coupling lens and semiconductor laser module | 385/33 | 385/93 | Yamagata; Michihiro et al. |
| US 6490100 B1 | 20021203 | Objective lens and optical head device | 359/719 | 359/708; 359/710; 359/718; 369/112.2 | Sasano; Tomohiko et al. |
| US 6465756 B2 | 20021015 | Method and apparatus for engraving | 219/121.6 | 219/121.85 | Tanaka; Yasuhiro et al. |
| US 6459101 B1 | 20021001 | Semiconductor device | 257/133 | 257/339; 257/342; 257/653; 257/E29.012; 257/E29.027; 257/E29.04; 257/E29.062; 257/E29.256 | Fujishima; Naoto et al. |
| US 6362989 B1 | 20020326 | Semiconductor memory with built-in cache | 365/45 | 365/230.09 | Tanaka; Yasuhiro et al. |
| US 6349000 B1 | 20020219 | Device for calculating diffraction efficiencies of a diffraction lens, lens with grating element, and optical system for reading | 359/575 | | Yamagata; Michihiro et al. |
| US 6320778 | 20011120 | Semiconductor memory with | 365/49 | 365/230.09 | Tanaka; |

| | | | | | |
|------------------|----------|--|-----------|---|----------------------------|
| B1 | | built-in cache | | | Yasuhiro et al. |
| US 6316794 B1 | 20011113 | Lateral high voltage semiconductor device with protective silicon nitride film in voltage withstanding region | 257/107 | 257/640; 257/760; 257/E23.134; 257/E29.015 | Saitou; Masaru et al. |
| US 6315937 B1 | 20011113 | Minutely working process | 264/293 | 264/313; 264/316 | Takeuchi; Kazutaka et al. |
| US 6312543 B1 | 20011106 | Process for producing tubular article for a fixation device | 156/218 | 156/272.4; 156/308.2; 156/309.6; 399/328 | Takeuchi; Kazutaka et al. |
| US 6304526 B1 | 20011016 | Optical head | 369/44.23 | 369/112.24; 369/44.14 | Nagashima; Kenji et al. |
| US 6301692 B1 | 20011009 | Method for designing layout of semiconductor integrated circuit, semiconductor integrated circuit obtained by the same method, and method for verifying timing thereof | 716/10 | 257/E27.108; 716/2 | Kumashiro; Shinichi et al. |
| US 6252337 B1 | 20010626 | Piezoelectric resonator and piezoelectric components using the same | 310/364 | 310/363 | Tanaka; Yasuhiro |
| US 6249450 B1 | 20010619 | Semiconductor memory with built-in cache | 365/49 | 365/230.09 | Tanaka; Yasuhiro et al. |
| US 6227127 B1 | 20010508 | Furnace material, heat resisting protection tube and protective member using the same sintered material | 110/338 | 110/323; 110/336; 501/100; 501/108; 501/110; 501/117 | Yamaguchi; Shinichi et al. |
| US 6174792 B1 | 20010116 | Method of manufacturing a semiconductor device | 438/585 | 257/E21.427; 257/E29.133; 257/E29.268; 438/425 | Kitamura; Akio |
| US 6139894 A | 20001031 | Flour blends for deep fried foods, steamed Chinese manju, dough sheets, okonomi-yaki, and takoyaki | 426/549 | 426/275; 426/283; 426/496; 426/622; 426/94 | Hoshino; Tsuguhiro et al. |
| US 6120191 A | 20000919 | Laser diode module | 385/93 | 385/33; 385/37; 385/92 | Asakura; Hiroyuki et al. |
| US 6093461 A | 20000725 | Heat-and corrosion-resisting protection tube | 428/34.4 | 428/213; 428/312.2; 428/332; 428/338; | Yamaguchi; Shinichi et al. |

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| | | | | 428/34.5; 428/702; 501/119; 501/120; 501/121; 501/127 | |
| US 6075276 A | 20000613 | ESD protection device using Zener diodes | 257/481 | 257/199; 257/355; 257/482; 257/546; 257/603; 257/606 | Kitamura; Akio |
| US 6025669 A | 20000215 | Piezoelectric resonator and piezoelectric components using the same | 310/364 | 310/355 | Tanaka; Yasuhiro |
| US 6011709 A | 20000104 | Semiconductor memory with built-in cache | 365/49 | 365/230.09 | Tanaka; Yasuhiro et al. |
| US 5989617 A | 19991123 | Method of assessing wheat or wheat flour regarding suitability for producing noodles | 426/549 | 127/29; 127/32; 426/451; 426/661 | Akashi; Hajime et al. |
| US 5986779 A | 19991116 | Multiple focus lens, an optical head apparatus and an optical information recording- reproducing apparatus | 359/19 | 359/16; 359/566; 359/570; 365/216 | Tanaka; Yasuhiro et al. |
| US 5983008 A | 19991109 | Method for designing layout of semiconductor integrated circuit, semiconductor integrated circuit obtained by the same method, and method for verifying timing thereof | 716/6 | 703/19 | Kumashiro; Shinichi et al. |
| US 5981997 A | 19991109 | Horizontal field effect transistor and method of manufacturing the same | 257/335 | 257/336; 257/343; 257/E21.417; 257/E29.054; 257/E29.256 | Kitamura; Akio |
| US 5929677 A | 19990727 | Phase locked loop having a voltage source responsive to a filtered phase difference output voltage and semiconductor device including the phase locked loop | 327/157 | 327/147; 327/148; 327/156; 331/17 | Murata; Toshihiko |
| US 5923569 A | 19990713 | Method for designing layout of semiconductor integrated circuit semiconductor integrated circuit obtained by the same method | 716/7 | 257/E27.108; 716/14; 716/18; 716/4 | Kumashiro; Shinichi et al. |

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| | | and method for verifying timing thereof | | | |
| US 5917217 A | 19990629 | Lateral field effect transistor and method of manufacturing the same | 257/343 | 257/339; 257/492; 257/E27.064; 257/E29.04; 257/E29.066; 257/E29.256 | Kitamura; Akio et al. |
| US 5916619 A | 19990629 | Fried instant noodles and method for manufacturing the same | 426/557 | 426/18; 426/28; 426/451; 426/555; 426/622 | Miyazaki; Eiji et al. |
| US 5909758 A | 19990608 | Supporting tool and method of installing same | 16/406 | 248/205.3; 248/205.6; 248/205.8; 248/206.3; 248/544 | Kitamura; Akio |
| US 5885878 A | 19990323 | Lateral trench MISFET and method of manufacturing the same | 438/302 | 148/DIG.126; 148/DIG.151; 257/E21.418; 257/E21.419; 257/E29.118; 257/E29.121; 257/E29.256; 257/E29.258; 257/E29.26; 438/297; 438/306; 438/307 | Fujishima; Naoto et al. |
| US 5875148 A | 19990223 | Semiconductor memory | 365/230.06 | 257/E21.659; 257/E27.097; 365/189.11 | Tanaka; Yasuhiro et al. |
| US 5844275 A | 19981201 | High withstand-voltage lateral MOSFET with a trench and method of producing the same | 257/335 | 257/336; 257/343; 257/E21.417; 257/E29.021; 257/E29.256; 257/E29.258; 257/E29.268 | Kitamura; Akio et al. |
| US 5830981 A | 19981103 | Process for producing polyethylene terephthalate | 528/283 | 524/714; 524/765; 524/773; 528/274; 528/308.2; 528/308.4; | Koreishi; Hiroshi et al. |

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| | | | | 528/308.5; 528/308.6; 528/499; 528/502R; 528/503 | |
| US 5824951 A | 19981020 | Electronic part comprising a casing with a narrow groove formed in the casing | 174/564 | 257/E23.193; 310/348; 361/728 | Tanaka; Yasuhiro |
| US 5814806 A | 19980929 | Code sheet representing multiple code information and method for producing the same | 235/494 | 235/462.01 | Tanaka; Yasuhiro et al. |
| US 5781466 A | 19980714 | Semiconductor memory with built-in cache | 365/49 | 365/149; 365/230.09 | Tanaka; Yasuhiro et al. |
| US 5760440 A | 19980602 | Back-source MOSFET | 257/328 | 257/329; 257/330; 257/334; 257/336; 257/492; 257/493; 257/E27.064; 257/E29.021; 257/E29.119; 257/E29.256; 257/E29.26; 257/E29.262; 257/E29.267; 257/E29.268 | Kitamura; Akio et al. |
| US 5757216 A | 19980526 | Electronic device using phase synchronous circuit | 327/156 | 327/147; 331/14; 331/17 | Murata; Toshihiko |
| US 5737237 A | 19980407 | Method and apparatus for data path circuit layout design and memory medium for causing computer to execute data path circuit layout design | 716/9 | 716/8 | Tanaka; Yasuhiro et al. |
| US 5705842 A | 19980106 | Horizontal MOSFET | 257/362 | 257/281; 257/335; 257/343; 257/358; 257/360; 257/361; 257/363; 257/373; 257/379; 257/449; 257/473; | Kitamura; Akio et al. |

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| | | | | 257/E29.258 | |
| US 5701026 A | 19971223 | Lateral trench MISFET | 257/510 | 257/335; 257/374; 257/E21.418; 257/E21.419; 257/E29.118; 257/E29.121; 257/E29.256; 257/E29.258; 257/E29.26 | Fujishima; Naoto et al. |
| US 5684641 A | 19971104 | Objective lens and optical head apparatus using the objective lens | 359/719 | 359/710; 359/711; 369/112.23 | Tanaka; Yasuhiro et al. |
| US 5659258 A | 19970819 | Level shifter circuit | 326/68 | 326/81; 326/83 | Tanabe; Tetsuya et al. |
| US 5652727 A | 19970729 | Semiconductor memory device | 365/203 | 365/202 | Tanaka; Yasuhiro et al. |
| US 5651006 A | 19970722 | Hierarchical network management system | 370/408 | 370/902; 709/223; 710/120 | Fujino; Shuji et al. |
| US 5636048 A | 19970603 | Equalizing amplifier, receiver using the same and preamplifier | 398/202 | 330/278; 330/291; 330/304; 398/208 | Kogure; Kazuhisa et al. |
| US 5633525 A | 19970527 | Lateral field effect transistor | 257/381 | 257/141; 257/343; 257/401; 257/E23.015; 257/E29.026; 257/E29.054; 257/E29.064; 257/E29.12; 257/E29.268 | Kitamura; Akio et al. |
| US 5631307 A | 19970520 | Photopolymerization initiator composition and photopolymerizable composition | 522/25 | 522/31 | Tanaka; Yasuhiro et al. |
| US 5596521 A | 19970121 | Semiconductor memory with built-in cache | 365/49 | 365/149 | Tanaka; Yasuhiro et al. |
| US 5591657 A | 19970107 | Semiconductor apparatus manufacturing method employing gate side wall self-aligning for masking | 438/202 | 257/E21.417; 257/E21.696; 257/E29.04; 257/E29.066; 257/E29.133; 257/E29.256; 257/E29.268; 438/208; | Fujishima; Naoto et al. |

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| | | | | 438/286; 438/305 | |
| US 5586255 A | 19961217 | Network management operation system and network management operation method | 709/223 | 715/700 | Tanaka; Yasuhiro et al. |
| US 5577223 A | 19961119 | Dynamic random access memory (DRAM) with cache and tag | 711/118 | 365/230.03; 365/49; 711/105 | Tanoi; Satoru et al. |
| US 5573888 A | 19961112 | Image transfer method | 430/256 | 430/257; 430/273.1 | Imabayashi; Arata et al. |
| US 5566115 A | 19961015 | Semiconductor memory device | 365/203 | 369/180; 369/202; 369/226 | Tanaka; Yasuhiro et al. |
| US 5535058 A | 19960709 | Focus error detecting element and optical head using the same | 359/711 | 359/719 | Tanaka; Yasuhiro et al. |
| US 5523599 A | 19960604 | High voltage MIS field effect transistor | 257/327 | 257/335; 257/343; 257/401; 257/653; 257/E29.012; 257/E29.027; 257/E29.12; 257/E29.256; 257/E29.258 | Fujishima; Naoto et al. |
| US 5511030 A | 19960423 | Semiconductor memory device and method of driving same | 365/203 | 365/190 | Tanaka; Yasuhiro et al. |
| US 5502323 A | 19960326 | Lateral type field effect transistor | 257/335 | 257/341; 257/401; 257/E23.145; 257/E23.152; 257/E29.027; 257/E29.12; 257/E29.122; 257/E29.133; 257/E29.256; 257/E29.268 | Kitamura; Akio et al. |
| US 5477496 A | 19951219 | Semiconductor memory device having circuits for precharging and equalizing | 365/203 | 365/202 | Tanaka; Yasuhiro et al. |
| US 5473471 A | 19951205 | Complex lens with diffraction grating | 359/569 | 359/566; 359/571; 359/574; 359/741; 359/742; 369/112.08 | Yamagata; Michihiro et al. |
| US 5471399 | 19951128 | Network management system | 716/11 | 370/241; | Tanaka; |

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| A | | and network status display method | | 714/47; 714/57; 715/733; 715/839; 715/969; 716/8 | Yasuhiro et al. |
| US 5461259 A | 19951024 | High-current integrated circuit | 257/758 | 257/401; 257/776; 257/E27.06 | Kitamura; Akio et al. |
| US 5451671 A | 19950919 | Method of purifying 2',3'-dideoxynucleosides | 536/27.12 | 536/27.14; 536/28.2 | Shiragami; Hiroshi et al. |
| US 5438665 A | 19950801 | Direct memory access controller for handling cyclic execution of data transfer in accordance with stored transfer control information | 710/25 | 710/28 | Taniai; Takayoshi et al. |
| US 5436486 A | 19950725 | High voltage MIS transistor and semiconductor device | 257/378 | 257/140; 257/146; 257/577; 257/E29.064; 257/E29.197 | Fujishima; Naoto et al. |
| US 5432370 A | 19950711 | High withstand voltage M I S field effect transistor and semiconductor integrated circuit | 257/339 | 257/336; 257/337; 257/343; 257/401; 257/409; 257/E27.06; 257/E29.012; 257/E29.015; 257/E29.017; 257/E29.027; 257/E29.066; 257/E29.256; 257/E29.258 | Kitamura; Akio et al. |
| US 5414917 A | 19950516 | Method of manufacturing piezoelectric resonator chips | 29/25.35 | 310/340 | Tanaka; Yasuhiro |
| US 5317792 A | 19940607 | Method of manufacturing piezoelectric resonator | 29/25.35 | 29/411 | Tanaka; Yasuhiro |
| US 5310895 A | 19940510 | Method for production of nucleoside derivatives by selective hydrolysis | 536/27.14 | 536/103; 536/27.6; 536/27.62; 536/27.63; 536/27.81; 536/28.2; 536/28.5; 536/28.53; | Shiragami; Hiroshi et al. |

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| | | | | 536/28.54 | |
| US 5270444 A | 19931214 | Process for treatment of polyethylene terephthalate, polyethylene terephthalate for molding purposes and process for preparation thereof | 528/499 | 528/308.2; 528/308.4; 528/308.5; 528/503 | Shiraki; Shigemi et al. |
| US 5241046 A | 19930831 | Process for treatment of polyethylene terephthalate, polyethylene terephthalate for molding purposes and process for preparation thereof | 528/499 | 528/308.2; 528/308.4; 528/308.5; 528/503 | Shiraki; Shigemi et al. |
| US 5213166 A | 19930525 | Pile driving and pile removing method | 173/1 | 175/65 | Watanabe; Naotaka et al. |
| US 5159302 A | 19921027 | Ladder type band-pass filter comprising of assembled plural vibrator blocks | 333/189 | 310/348; 310/355 | Tanaka; Yasuhiro |
| US 5119487 A | 19920602 | DMA controller having programmable logic array for outputting control information required during a next transfer cycle during one transfer cycle | 710/22 | | Taniai; Takayoshi et al. |
| US 5068849 A | 19911126 | Cyclic data transmission method | 370/509 | 370/452 | Tanaka; Yasuhiro |
| US 5043935 A | 19910827 | Data transfer system for rearranging data units using intermediate temporary registers | 710/66 | | Taniai; Takayoshi et al. |
| US 5033017 A | 19910716 | Programmable logic array with reduced power consumption | 708/232 | 326/105; 326/38; 326/44 | Taniai; Takayoshi et al. |
| US 4998246 A | 19910305 | Method for transmission of cyclic data | 370/452 | | Tanaka; Yasuhiro |
| US 4980890 A | 19901225 | Semiconductor integrated circuit | 714/735 | | Taniai; Takayoshi et al. |
| US 4927247 A | 19900522 | Objective lens for optical disk system and optical head using the same | 359/708 | | Tanaka; Yasuhiro et al. |
| US 4842388 A | 19890627 | Single optical lense with diffraction limit converging capability and wide field angle | 359/718 | | Tanaka; Yasuhiro et al. |
| US 4786772 A | 19881122 | Induction heating coil for heat-treating metallic tubes | 219/643 | 219/632; 219/674; 266/129 | Umemoto; Tadahiro et al. |
| US 4695756 A | 19870922 | Laminated terminal layer for electronic component | 310/355 | 310/326; 310/354; 310/364 | Tanaka; Yasuhiro |

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| US 4693571 A | 19870915 | Support and drive unit for mirror devices | 359/874 | 359/877 | Kimura; Sakae et al. |
| US D291461 S | 19870818 | Reconfigurable toy vehicle | D21/582 | | Kitamura; Akio |
| US D290723 S | 19870707 | Reconfigurable toy vehicle | D21/581 | | Kitamura; Akio |
| US D290630 S | 19870630 | Reconfigurable toy tank | D21/580 | | Kitamura; Akio |
| US D290481 S | 19870623 | Reconfigurable toy boat | D21/580 | | Kitamura; Akio |
| US 4656385 A | 19870407 | Housing having grooves therein for mounting a piezoelectric ladder filter | 310/348 | 174/549; 174/560; 333/189 | Tanaka; Yasuhiro |
| US 4641055 A | 19870203 | Piezoelectric resonator with notched sides | 310/368 | 310/312 | Tanaka; Yasuhiro |
| US 4582469 A | 19860415 | Intake valve apparatus in air pump | 417/571 | 137/855 | Asaka; Urataro et al. |
| US 4551074 A | 19851105 | Air pump apparatus | 417/366 | 417/415; 417/440; 92/158 | Asaka; Urataro et al. |
| US 4323865 A | 19820406 | Ladder-type piezoelectric filter | 333/187 | 310/348; 333/189; 333/190 | Tanaka; Yasuhiro et al. |